

~~10/533058~~

INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Application Number	10/533058
	Filing Date	
	First Named Inventor	Mihai A. T. Sanduleanu
	Art Unit	
	Examiner Name	
	Attorney Docket Number	NL02 1079 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number (ctry ² -no. ³ -kind ⁴ , if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear
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/J.P./		JAFAR SAVOJ ET AL: "DESIGN OF HALF-RATE CLOCK AND DATA RECOVERY CIRCUITS FOR OPTICAL COMMUNICATION SYSTEMS" PROCEEDINGS OF THE 38 TH ANN. DESIGN AUTO. CONF. (DAC). JUNE 18-22, 2001			
/J.P./		YOON, CHAN G, ET AL: "DIGITAL LOGIC IMPLEMENTATION OF THE QUADRICORRELATORS FOR FREQUENCY DETECTOR" DEPT. OF ELECTRONICS ENGINEERING, SEOUL NAT. UNIV. SHINLIM DONG, KWANAK KU, SEOUL, 151-742, KOREA			

Examiner Signature	/James Perez/	Date Considered	01/17/2008
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